

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2239	(program\$4 or instruct\$3 or request\$3) with (first or primary) with thread	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/05/02 15:40
L2	663	((cach\$3 near4 (line or data or entry or address\$3)) with (modification or modified or modify or modif\$4 or replac\$3 or updat\$3)) same (ownership or owner-ship)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/05/02 15:42
L3	1	(monitor near4 (address\$3 or instruct\$3 or request\$3)) and 1 and 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/05/02 15:43
L4	227	((cach\$3 near4 (line or data or entry or address\$3)) same (modification or modified or modify or modif\$4 or replac\$3 or updat\$3)) and 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/05/02 15:43
L5	39	(monitor near4 (address\$3 or instruct\$3 or request\$3)) and 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/05/02 15:54
L6	4	((suspend\$3 or stop\$4) with (device or logic or circuit)) same thread) and 5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/05/02 15:45
L7	16	"6018759".uref.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/05/02 15:48
L10	0	((suspend\$3 or stop\$4 or interrupt\$3) with (logic or device or circuit\$3)) and ((resum\$3 or retain\$3 or retrun\$3) with (logic or device or circuit\$3)) and 7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/05/02 15:51
L12	13	((program\$4 or instruct\$3 or request\$3) same thread) and 7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/05/02 15:56

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L13	3	((suspend\$3 or stop\$4 or interrupt\$3) with (logic or device or circuit\$3)) and 12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/05/02 15:55
L16	8	((("6341347") or ("6351808") or ("6401155") or ("6505229") or ("6671795") or ("20030126379") or ("20030126416") or ("20030126186")).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/02 15:56
L17	8	((program\$4 or instruct\$3 or request\$3) same thread) and 16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/05/02 15:56
L19	90	(thread same (instruct\$3 or instruction or program\$4 or request\$3)) and ((cach\$3 with (line or entry or address\$3 or data)) same ownership) and (suspend\$3 or interrupt\$3 or stop\$4) and (resum\$3 or restrain\$3 or return\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/05/02 16:06
L20	20	(monitor with (address\$3 or request\$3 or instruct\$3)) and 19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/05/02 16:07
L21	15	partition\$3 and 20	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/05/02 16:10

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... the multipath(8) **cache** file **logic** in favour of a multipathd<->multipath unix ... remove over-complicated and **thread**-confusing daemon initial clone() w/ ...
christophe.varoqui.free.fr/ - 15k - Apr 30, 2006 - [Cached](#) - [Similar pages](#)

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Once you understand the **logic** of the garbage collection process, it's easy to understand how ... If so, the **thread** is **suspended**, the GC runs to completion, ...

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... pthread_suspend/pthread_resume allows to **suspend** or **resume** a **thread** in a ... All necessary bus arbitration and memory support **logic** are integrated into ...

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[PDF] Synchronisation in a Multithreaded Processor

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Thread. Context. **Thread**. Context. **Thread**. Context. Integer. **Logic** ... In order to synchronise, it must be possible to identify, **suspend** and **resume**. **threads**. ...

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Flow Java integrates single assignment variables (**logic** variables) into Java. This paper ... The **thread resumes** execution when another **thread** calls ...

www.sics.se/~frej/flow_java/ DrejhammarSchulte_CICLOPS_2004.pdf - [Similar pages](#)

EP1224535 Sun european software patent - Thread switch logic in a ...

[0016] A processor includes anti-aliasing **logic** coupled to an L1 **cache** so that the L1 **cache** is ... Java(TM) methods including "**suspend**", "**resume**", "**sleep**", ...

gauss.ffii.org/PatentView/EP1224535 - 157k - [Cached](#) - [Similar pages](#)

Linux-Kernel Archive: APM Suspend Problem

... Next in **thread**: Felipe Alfaro Solana: "Re: APM **Suspend** Problem" ... ac97_codec: AC97

Audio codec, id: CRY20 (Cirrus **Logic** CS4297A rev B) ...

www.ussg.iu.edu/hypermail/linux/kernel/0311.3/0696.html - 14k - [Cached](#) - [Similar pages](#)

Linux-Kernel Archive: [PROBLEM] 2.6.3-rc1: still no suspend/resume ...

... Next in **thread**: Ari Pollak: "Re: [PROBLEM] 2.6.3-rc1: still no **suspend/resume** on ... In any case: given that the **logic** is right, I propose to change the ...

www.ussg.iu.edu/hypermail/linux/kernel/0402.1/0052.html - 46k - [Cached](#) - [Similar pages](#)

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[PS] [17] W. Hackbusch, editor. Multigrid Methods and Applications ...

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Sparcle's fabrication is being supported by LSI **Logic**. ... 4. switch blocking - **suspend** the **thread** without unloading it, and switch to a different context. ...

www.lcs.mit.edu/pubs/ps/MIT-LCS-TM-454.ps.gz - [Similar pages](#)

Cache block replacement scheme based on directory control bit set ...

To **resume** the once-**suspended thread**, it is necessary to identify the **thread** ... notify the processor and **logic** circuit 422f of the **cache** hit or **cache** miss. ...

www.freepatentsonline.com/5535361.html - 65k - [Cached](#) - [Similar pages](#)

[PDF] [Formal specification and verification of the C thread model](#)

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Interrupt, **Thread.Suspend** and **Thread.Resume** are not included ... now use the syntax of ASM **logic** as introduced in [16] and extended in [17]. ...

www.inf.ethz.ch/~staerk/pdf/threads04.pdf - [Similar pages](#)

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solutions opt to **suspend** checkpointed **threads** to a nice state in userspace [15,16]. ...
 increase the **logic** required to provide a consistent checkpoint. ...
www.cs.hut.fi/~pooka/school/thesis/kantee-appchkpt.pdf - [Similar pages](#)

Multiple thread multiple data predictive coded parallel processing ...

A synchronous **logic** device that reads instructions from the code memory and places the instructions into a **cache**. Sequence control **logic** in combination with ...

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3cm

Mobile **threads** complete our **Logic** Programming based Internet programming ... **resumes** a **suspended thread** `thread_join(Thread)` % waits until a **thread** ...

logic.csci.unt.edu/tarau/research/98/GULP98.html - 56k - [Cached](#) - [Similar pages](#)

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(a) DBMS **threads** **suspend** and **resume** execution at different places of the code (performing ... **logic**. We call stray the **threads** that leave a team. ...

www.vldb.org/conf/2004/RS18P1.PDF - [Similar pages](#)

esp@cenet description view

On receipt of an "event **suspend**" packet the special event **logic** 44 will cause ... To enable the CPU to **resume** the interrupted **thread** after responding to the ...

v3.espacenet.com/textdes?IDX=EP0953914&

CY=ep&LG=en&DB=EPODOC&QPN=EP0953914 - 91k - [Cached](#) - [Similar pages](#)

Multi-core and Multi-threaded SoCs Present New Debugging Challenges

Schedulers use context records to **suspend** and **resume** tasks. ... The first provides extra **logic** to present OSes with an appearance of multiple virtual ...

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logic, as well as implementation ideas for SCORE for processor architectures. ... traditional data blocking for better **cache** behavior, but in our case the ...

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[PS] A Reconfigurable Architecture for High Speed Computation by ...

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... **suspend** and **resume**) the computation of each **thread** on the pipeline in one clock cycle ... Control **Logic** Block (PLD). (1) single **thread** Data Interconnect ...
darwin.esys.tsukuba.ac.jp/Papers/arch.ps - [Similar pages](#)

MBlog : Greg Wilkins

... as there is no mechanism in Java to **suspend** a **thread** and then **resume** it later. ...
Filters provide an easy way of adding **logic** to a webapp, ...
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